

NXP LCD Graphics Drivers

V8.2 / 05-Feb-2014 / ML

Device Weblink	Max Display Resolution Rows x Cols	Multiplex Rates	VDD1		VDD2		VLCD		Frame Frequency (Hz)	On-Chip VLCD Generation (Charge Pump)	On Chip VLCD Temperature Compensation	Tamb		Interface	Package	AEC-Q100 compliant	Status	Demo Board
			[min] (V)	[max] (V)	[min] (V)	[max] (V)	[min] (V)	[max] (V)				[min] (°C)	[max] (°C)					
PCA8539DUG	18 x 100	Mux 1:12; Mux 1:18	2.5	5.5	2.5	5.5	4	16	45-360 ¹⁾	Y	Y	-40	105	I2C / SPI	Gold-bumped die	Y	released	OM13503
PCF8531U	34 x 128 or 33 x 128 + 128 icons	Mux 1:17, Mux 1:26, Mux 1:34	1.8	5.5	2.5	4.5	4	9	66	Y	Y	-40	85	I2C	Gold-bumped die	N ⁵⁾	released	
PCF8811U	80 x 128 or 79 x 129 + 128 icons	Mux 1:16 to Mux 1:80 in steps of 8	2	3.3	1.8	3.3	3	9	30-60 ¹⁾	Y	Y	-40	85	I2C / SPI / Parallel	Gold-bumped die	N ⁵⁾	released	

¹⁾ SW programmable ²⁾ HW selectable ³⁾ release H1 2014 ⁴⁾ release H2 2014 ⁵⁾ not automotive qualified but used successfully for many years in automotive